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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Title: MULTIMEDIA INTERFACE HAVING A PROCESSOR AND RECONFIGURABLE LOGIC
(As Amended Herewith)

PRELIMINARY AMENDMENT

IN THE CLAIMS

Cancel all of the existing claims (1-22), and *add* the following (23-xx)

- Sub B1
- A2
23. Multimedia interface, comprising:
an integrated circuit (IC) chip;
a block of reconfigurable logic incorporated on the IC chip; and
a block of media processor with a virtual instruction set capable of implementing a variety of multimedia algorithms incorporated on the IC chip separately from the reconfigurable logic block.
24. The multimedia interface, according to claim 23, further comprising audio and/or video CODEC incorporated on the IC chip.
25. The multimedia interface, according to claim 23, further comprising a phase locked loop (PLL) circuitry incorporated on the IC chip.
26. The multimedia interface according to claim 23, further comprising a programmable, fast serial interface core incorporated on the IC chip.
27. The multimedia interface according to claim 23, further comprising a programmable CPU interface core incorporated on the IC chip.
28. The multimedia interface according to claim 23, further comprising a programmable memory interface (PMI) core incorporated on the IC chip.